



Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. SUN-P5447		Serial No. 09/823,207		
Information Disclosure Statement by Applicant				Applicant: Partha P. Tirumalai et al.				
(Use several sheets if necessary)				Filed: March 30, 2001 Group: 2122				
U.S. Patent Documents								
Init.		Document No.	Date	Name	Class	Subclass	Filing Date	
<i>SW</i>	1	5,493,675	02/20/1996	Faiman, Jr. et al.	395	700	05/26/1994	
<i>SW</i>	2	6,078,744	06/20/2000	Wolezko et al.	395	705	08/01/1997	
<i>SW</i>	3	6,289,505 B1	09/11/2001	Goebel	717	9	11/18/1997	
<i>SW</i>	4	6,292,938 B1	09/18/2001	Sarkar et al.	717	6	12/02/1998	
<i>SW</i>	5	6,295,643 B1	09/25/2001	Brown et al.	717	9	12/16/1998	
Foreign Documents								
Technology Center 2100								
Translation								
Init.		Document No.	Date	Country	Class	Subclass	Yes	No
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)								
<i>SW</i>	6	T. Williams, "Compiler Technology Tracks CPU Architecture Headway", <u>Embedded Systems Development</u> , pp. 41-45, August 2000.						
Examiner <i>William J. Wood</i>					Date Considered <i>6/4/04</i>			
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.								